



IN THE U.S. PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

APPLICANT: Sheng Zhao et al.

SERIAL NO.: 09/823,159

FILING DATE: 03/30/2001

EXAMINER: Patel, Nimesh G.

ART UNIT: 2112

ATTORNEY'S DOCKET NO.: 871.0013 USU

TITLE: Programmable CPU/Interface Buffer Structure Using Dual Port RAM

Commissioner for Patents

Alexandria, VA 22313

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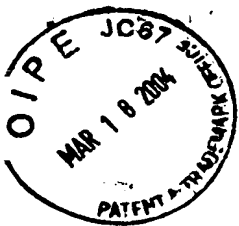
RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

Sir:

This paper is herewith filed in response to the Examiner's Office Action mailed on 03/11/2004 for the above-captioned U.S. Patent Application. This paper is deemed to be filed within the shortened statutory period, and no petition or fee for an extension of time is required. However, should the undersigned attorney be mistaken, please consider this a petition for any extension of time that may be required to maintain the pendency of this Patent Application, and charge deposit account no.: 50-1924 for any required fee deficiency.

Please amend the application as shown below.

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CB  
ah  
3/22/04



S.N. 09/823,159  
Art Unit: 2112

### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. BOX 1450, Alexandria, VA 22313-1450.

3/16/04  
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Claine L. Muen  
Name of Person Making Deposit

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